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**Complete if Known**

Application Number	10/782,362
Filing Date	Herewith
First Named Inventor	Sujatha Kashyap
Art Unit	2187
Examiner Name	H. Nguyen
Attorney Docket Number	AUS920030955US1

Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
Hr	C1	JIAN HUANG, et al.; Page-Mapping Techniques for CC-NUMA Multiprocessors; 1997; pp 91-104; (0-7803-4229-1/97 1997 IEEE); US	
	C2	RUI MIN, et al., Improving Performance of Large Physically Indexed Caches by Decoupling Memory Addresses from Cache Addresses, Nov. 2001, Vol 50, No 11; IEEE Transactions; US	
	C3	JACK L. LO, et al.; An Analysis of Database Workload Performance on Simultaneous Multithreaded Processors; US	
	C4	R. E. KESSLER et al.; Page Placement Algorithms for Large Real-Indexed Caches; Nov. 1992; pp 338-359; Vol 10, No 4; ACM Transactions on Computer Systems; US	
✓	C5	JOCHEN LIEDTKE; Colorable Memory; 11/10/96; pp 1-4; IBM; US	

Examiner Signature	<i>[Signature]</i>	Date Considered	4/3/06
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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